

### REMARKS

The Applicants respectfully request reconsideration of this application in view of the above amendments and the following remarks.

#### 35 U.S.C. §102(e) Rejection - Poisner '669

The Examiner has rejected claims 1-17, 23-24 and 26-27 under 35 U.S.C. §102(e) as being anticipated by U.S. Patent No. 7,076,669 issued to Poisner et al. (hereinafter "Poisner '669").

Claim 1 pertains to:

*"A computer system, comprising:  
a chipset;  
an internal component of the computer system;  
a bus coupled to the chipset to communicate a trusted data cycle to the internal component of the computer system;  
a docking connector; and  
a secured docking circuit coupled to the bus and coupled between the bus and a docking connector to scan for the trusted data cycle, detect the trusted data cycle, and provide a filtering mechanism to prevent the trusted data cycle from being provided to a device external to the computer system through the docking connector".*

Poisner '669 does not disclose these limitations. In particular, Poisner '669 does not disclose a **secured docking circuit coupled to the bus and coupled between the bus and a docking connector to scan for the trusted data cycle, detect the trusted data cycle, and provide a filtering mechanism to prevent the trusted data cycle from being provided to a device external to the computer system through the docking connector.**

Poisner '669 discusses a method and apparatus for communicating securely with a token. See e.g., the Title. However, Poisner '669 does not disclose the claimed **secured docking circuit that is coupled between the bus and the docking connector in combination with the**

other claim limitations. Furthermore, in rejecting former claim 1, the Examiner has relied upon the operations of two different components described at column 4, lines 26-33 and column 5, lines 6-13, respectively. Column 4, lines 26-33 describe operations of the processor bus interface (I/F) logic 104. Column 5, lines 6-13 describe operations of the token 155. In contrast, claim 1 makes it clear that the secured docking circuit coupled between the bus and a docking connector performs each of the **scanning** for the trusted data cycle, the **detecting** the trusted data cycle, and the **providing** the filtering mechanism to prevent the trusted data cycle from being provided to the device external to the computer system through the docking connector. In other words, a single component with a specifically defined coupling within the computer system performs each of these operations instead of two different components at two different locations within the computer system.

For at least one or more of these reasons, claim 1 and its dependent claims are not anticipated by Poisner '669.

Independent claims 7 and 10, and their respective dependent claims, are believed to be allowable for one or more similar reasons.

### 35 U.S.C. §103(a) Rejection – Poisner '669, Poisner '143

The Examiner has rejected claims 18 and 19 under 35 U.S.C. §103(a) as being unpatentable over Poisner '669 in view of U.S. Patent Publication No. 2004/0268143 issued to Poisner (hereinafter "Poisner '143").

Without admitting that these references are prior art under 35 U.S.C. §103(a), and without admitting that these references may be combined, Applicants elect not to address the rejection of these dependent claims at this point, since the independent claims from which these dependent claims depend have been shown to be allowable over Poisner '669.

**35 U.S.C. §103(a) Rejection – Poisner '669, Yanagisawa**

The Examiner has rejected claim 25 under 35 U.S.C. §103(a) as being unpatentable over Poisner '669 in view of U.S. Patent No. 6,519,669 issued to Yanagisawa (hereinafter "Yanagisawa").

Without admitting that Poisner '669 is prior art under 35 U.S.C. §103(a), and without admitting that these references may be combined, Applicants elect not to address the rejection of this dependent claim at this point, since the independent claim from which this dependent claim depends has been shown to be allowable over Poisner '669.

**35 U.S.C. §103(a) Rejection – Poisner '669, Poisner '143, Probst**

The Examiner has rejected claims 20-22 under 35 U.S.C. §103(a) as being unpatentable over Poisner '669 in view of Poisner '143 and in view of U.S. Patent No. 5,982,899 issued to Probst (hereinafter "Probst").

Without admitting that Poisner '669 and/or Poisner '143 are prior art under 35 U.S.C. §103(a), and without admitting that these references may be combined, Applicants elect not to address the rejection of these dependent claims at this point, since the independent claim(s) from which these dependent claims depends have been shown to be allowable over Poisner '669.

### Conclusion

In view of the foregoing, it is believed that all claims now pending patentably define the subject invention over the prior art of record and are in condition for allowance. Applicants respectfully request that the rejections be withdrawn and the claims be allowed at the earliest possible date.

### Request For Telephone Interview

The Examiner is invited to call Brent E. Vecchia at (303) 740-1980 if there remains any issue with allowance of the case.

### Request For An Extension Of Time

The Applicants respectfully petition for an extension of time to respond to the outstanding Office Action pursuant to 37 C.F.R. § 1.136(a) should one be necessary. Please charge our Deposit Account No. 02-2666 to cover the necessary fee under 37 C.F.R. § 1.17 for such an extension.

### Charge Our Deposit Account

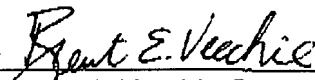
Please charge any shortage to our Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 12/4/2007

By



Brent E. Vecchia, Reg. No. 48,011

Tel.: (303) 740-1980 (Mountain Time)

1279 Oakmead Parkway  
Sunnyvale, California 94085-4040